

IN THE CLAIMS

Please amend the claims as follows:

Claims 1-2 (Canceled)

Claim 3 (Withdrawn): A method of manufacturing a multi-layer printed circuit board comprising at least the following steps (a) to (d):

- (a) forming lower interlayer resin insulating layers on both sides of a core substrate, respectively;
- (b) forming through holes penetrating said core substrate and said lower interlayer resin insulating layers;
- (c) forming upper interlayer resin insulating layers on said lower interlayer resin insulating layers, respectively; and
- (d) forming via holes in said upper interlayer resin insulating layers, the via holes connected to external connection terminals and formed right on part of said through holes.

Claim 4 (Withdrawn): A method of manufacturing a multi-layer printed circuit board comprising at least the following steps (a) to (g):

- (a) forming lower interlayer resin insulating layers on both sides of a core substrate, respectively;
- (b) forming through holes penetrating said core substrate and said lower interlayer resin insulating layers;
- (c) filling resin filler in said through holes;
- (d) polishing and flattening the resin filler pouring from said through holes;
- (e) forming conductor layers covering exposed surfaces of said resin filer from said through holes;

- (f) forming upper interlayer resin insulating layers on said lower interlayer resin insulating layers, respectively; and
- (g) forming via holes in said upper interlayer resin insulating layers and forming the via holes right on part of said through holes so as to be connected to external connection terminals.

Claim 5 (Withdrawn): A multi-layer printed circuit board having interlayer resin insulating layers on both sides of a core substrate, respectively, through holes provided to penetrate the core substrate and filled with resin filler, the interlayer resin insulating layers and conductor circuits provided, wherein

said resin filler contains an epoxy resin, a curing agent and 10 to 50% of inorganic particles.

Claim 6 (Withdrawn): A multi-layer printed circuit board having interlayer resin insulating layers formed on both sides of a core substrate, respectively, through holes provided to penetrate the core substrate and filled with resin filler, plated covers provided, the interlayer resin insulating layers and conductor circuits provided, wherein

said resin filler contains an epoxy resin, a curing agent and 10 to 50% of inorganic particles.

Claim 7 (Withdrawn): A multi-layer printed circuit board according to claim 5 or 6, wherein

said inorganic particles contain one type or more selected from a group consisting of aluminum compounds, calcium compounds, potassium compounds, magnesium compounds and silicon compounds.

Claim 8 (Withdrawn): A multi-layer printed circuit board according to claim 6 or 7, wherein

a shape of said inorganic particles is one of a spherical shape, a circular shape, an ellipsoidal shape, a pulverized shape and a polygonal shape.

Claim 9 (Withdrawn): A multi-layer printed circuit board according to any one of claim 6 to 8, wherein

rough layers are provided on the conductor layers of said through holes, respectively.

Claim 10 (Withdrawn): A method of manufacturing a multi-layer printed circuit board having interlayer resin insulating layers provided on both sides of a core substrate, for forming the interlayer resin insulating layers through the following steps (a) to (e):

- (a) a formation step of forming through holes penetrating the both sides of the printed circuit board;
- (b) a filling step of filling resin filler containing an epoxy resin and 10 to 50% of inorganic particles;
- (c) a drying step and a polishing step;
- (d) a hardening step; and
- (e) a cover plating step.

Claim 11 (Withdrawn): A method according to claim 10, wherein in said polishing step (c), a buffing step is conducted at least once or a plurality of times.

Claim 12 (Withdrawn): A method according to claim 10 or 11, wherein in said step (a), a step of forming rough layers is conducted.

Claim 13 (Withdrawn): A multi-layer printed circuit board having buildup layers on both sides of a core substrate, respectively, said buildup layer having interlayer resin insulating layers and conductor layers alternately provided, the conductor layers connected to one another by via holes, wherein

through holes filled with resin filler are formed to penetrate said core substrate and lower interlayer resin insulating layers formed on the both sides of the core substrate; and via holes filled with said resin filler are formed in said lower interlayer resin insulating layers.

Claim 14 (Withdrawn): A multi-layer printed circuit board according to claim 13, wherein

the conductor layers are formed to cover exposed surfaces of the resin filler filled in the via holes of said lower interlayer resin insulating layers; and via holes are formed right on the via holes through the conductive layers, respectively.

Claim 15 (Withdrawn): A method of manufacturing a multi-layer printed circuit board comprising at least the following steps (a) to (g):

- (a) forming lower interlayer resin insulating layers on both sides of a core substrate, respectively;
- (b) forming penetrating holes in said core substrate and said lower interlayer resin insulating layers, the penetrating holes becoming through holes;

- (c) forming openings in said lower interlayer resin insulating layers, the openings becoming via holes;
- (d) forming conductive films in said penetrating holes and said openings to thereby provide the through holes and the via holes, respectively;
- (e) filling resin filler in said through holes and said via holes;
- (f) polishing and flattening the resin filler pouring out of said through holes and said via holes; and
- (g) forming conductor layers covering exposed surfaces of said resin filler from said through holes and said via holes, respectively.

Claim 16 (Withdrawn): A method of manufacturing a multi-layer printed circuit board comprising at least the following steps (a) to (i):

- (a) forming lower interlayer resin insulating layers on both sides of a core substrate, respectively;
- (b) forming penetrating holes in said core substrate and said lower interlayer resin insulating layers, the penetrating holes becoming through holes;
- (c) forming openings in said lower interlayer resin insulating layers, the openings becoming via holes;
- (d) forming conductive films in said penetrating holes and said openings to provide the through holes and the via holes;
- (e) filling resin filler in said through holes and said via holes;
- (f) polishing and flattening the resin filler pouring out of said through holes and said via holes;
- (g) forming conductor layers covering exposed surfaces of said resin filler from said through holes and said via holes;

- (h) forming upper interlayer resin insulating layers on said lower interlayer resin insulating layers, respectively; and
- (i) forming via holes in said upper interlayer resin insulating layers and right on part of said via holes.

Claim 17 (Withdrawn): A method of manufacturing a multi-layer printed circuit board comprising at least the following steps (a) to (e):

- (a) forming lower interlayer resin insulating layers on both sides of a core substrate, respectively;
- (b) forming penetrating holes in said core substrate and said lower interlayer resin insulating layers, the penetrating holes becoming through holes;
- (c) forming openings in said lower interlayer resin insulating layers, the openings becoming via holes;
- (d) conducting a de-smear process to said penetrating holes by an acid or an oxidizer and conducting a roughing process to surfaces of the lower interlayer resin insulating layers; and
- (e) forming conductive films on said penetrating holes and said openings to provide the through holes and the via holes, respectively.

Claim 18 (Withdrawn): A method according to claim 17, wherein said core substrate is made of one of a glass epoxy resin, an FR4 resin, an FR5 resin and a BT resin;

each of said lower interlayer resin insulating layers contains at least one of an epoxy resin, a phenol resin, a polyimide resin, a polyphenylene resin, a polyolefin resin and a fluorocarbon resin; and

said oxidizer contains one of a chromic acid and permanganate.

Claim 19 (Withdrawn): A method according to claim 17, wherein
said acid contains one type or more selected from a group consisting of a sulfuric acid,
a hydrochloric acid, a nitric acid, a phosphoric acid and a formic acid.

Claim 20 (Withdrawn): A method according to claim 17, wherein
said oxidizer contains one of a chromic acid and permanganate.

Claim 21 (Withdrawn): A method of manufacturing a multi-layer printed circuit
board comprising at least the following steps (a) to (d):

- (a) forming through holes in a core substrate;
- (b) forming rough layers on said through holes, respectively;
- (c) polishing and flattening surfaces of lands of said through holes; and
- (d) filling resin filler in said through holes and forming resin layers.

Claim 22 (Withdrawn): A method according to claim 21, wherein
said rough layers are copper oxide layers.

Claim 23 (Withdrawn): A method according to claim 21, wherein
said rough layers are formed by etching.

Claim 24 (Withdrawn): A method according to claim 21, wherein
said rough layers are needle alloy layers made of copper-nickel-phosphorous.

Claim 25 (Withdrawn): A method according to any one of claims 21 to 24, wherein said resin filler is one selected from a group consisting of a mixture of an epoxy resin and organic filler, a mixture of an epoxy resin and inorganic filler and a mixture of an epoxy resin and inorganic fiber.

Claim 26 (New): A multi-layer printed circuit board comprising:
a core substrate having opposite first and second sides;
first and second buildup layers provided on the first and second sides of the core substrate respectively, the first buildup layer having first interlayer resin insulating layers and first conductor layers alternately provided, the second buildup layer having second interlayer resin insulating layers and second conductor layers alternately provided;
through holes each electrically connecting the first conductor layers and the second conductor layers at opposite ends of each of the through holes;
through hole lands each being electrically connected to each of the through holes between the opposite ends of each of the through holes;
first via holes provided right on the ends of said through holes to be electrically connected to the through holes; and
second via holes provided to be electrically connected to the through hole lands.

Claim 27 (New): A multi-layer printed circuit board according to claim 26, wherein resin filler is filled in said through holes and the first and second conductor layers are formed to cover exposed surfaces of the resin filler from the through holes; and the first via holes are formed on said conductor layers.